

# STUDENT PAPER CONTEST AND TECHNICAL SYMPOSIUM (SPCTS-2007)

September 28-30 2007

Venue: DA-IICT, Gandhinagar, India

URL: [www.ewh.ieee.org/r10/gujarat](http://www.ewh.ieee.org/r10/gujarat)  
<http://iee.daiict.ac.in/aisc/events/spcts.htm>

Jointly Organised  
by



**General Chair**  
Dr. S. C. Sahasrabudhe  
Director, DA-IICT  
Mr. N.T. Nair  
Chair, IEEE India Council



**Organizing Chair**  
Mr. Deepak Mathur  
**Co-ordinator-AISC**  
Dr. Anil Roy  
**Co-ordinator-SPCTS**  
Mr. Nirbhay Chaubey

## THEME

Networking to Share for Excellence

## WHO CAN PARTICIPATE

Open to all those Undergraduate & Postgraduate Students with basic degree in Electrical, Electronics, Computer Engineering and Computer Science or any other field of interest of an IEEE Society.

## HOSTED BY :



IEEE Student Branch, DA-IICT  
Post Bag No. 4  
Near Indroda Circle  
Gandhinagar-382007  
Gujarat, India  
Ph: +91- 79- 3051 0620  
URL:<http://iee.daiict.ac.in/aisc/events/spcts>

## ABOUT AISC –2007

**All India Student Congress (AISC)**, an annual event of the IEEE India Council, brings the IEEE student members of various student branches from India and provides a platform for sharing their views and experiences. This excellent opportunity for networking allows them to discuss issues related to their technical and professional development as well.

AISC 2007 is being held on 28, 29 and 30th September 2007 at DA-IICT, Gandhinagar, Gujarat, India

## ABOUT SPCTS - 2007

“**Student Paper Contest and Technical Symposium**” (SPCTS) is an annual national level event of the IEEE Gujarat Section started in year 2003 and since then it has been attracted students researchers to discuss together their ideas and experiences.

DA-IICT, Gandhinagar and IEEE Gujarat Section are proud to organize the 4th “**Student Paper Contest and Technical Symposium**” (SPCTS) along with the **IEEE All India Student Congress (AISC-2007)** to be held from September 28 to September 30, 2007 in the beautiful campus of DA-IICT, city of Gandhinagar, Gujarat, India for the Undergraduate (UG) and Postgraduate (PG) Students with a view to encourage them for research in depths of Computer, Electrical, Electronics Engineering and Computer Science. SPCTS – 2007 follows the first three highly successful events held at Gujarat University, Ahmedabad in 2003, UVPCE, Mehsana in 2005 and ADIT, Vallabh Vidyanagar in 2006.

The symposium will provide the perfect forum for the students to discuss new ideas and break-through, exchange up to date knowledge, experiences and views on key areas of Engineering and Computer Science.

Accepted papers are likely to be included in the IEEE Xplore database subject to acceptance by the rigorous standard peer review process.

## PRIZES

Three prizes in Indian Rupees and a Certificate for each of the three categories for Undergraduate SPC and Postgraduate SPC will be awarded as follows.

Category	1st Prize	2nd Prize	3rd Prize
Electrical Engg.	3,000 /-	2,000 /-	1,000 /-
Electronics Engg.	3,000 /-	2,000 /-	1,000 /-
Computer Science and Engg.	3,000 /-	2,000 /-	1,000 /-

The Symposium will also offer Exciting Keynote Speakers and Special Sessions.

## TOPIC OF THE SYMPOSIUM

The program committee solicits high quality research papers (written in English) describing mature results or on-going work. Topics for submission include, but are not limited to the following:

**ELECTRICAL ENGINEERING:** Advanced Electric Machinery, Applied Superconductivity, Biomedical Engineering, Computer Applications in Power System, Data Compression/ Storage, Electric Vehicles, Electrical Drives, Non-conventional Energy sources, Pattern Recognition and Power Systems, Power Electronics, Process Control and Vehicular Technology etc.

## ELECTRONICS AND COMMUNICATION ENGINEERING:

Antenna System, ASIC Design Technology, Automotive Electronics, ATM, Cellular Network and Paging System, Communication System, Consumer Electronics, Control System, Digital Signal Processing, Fibreoptic Communication System, High Speed Network, ISDN, Mobile Computing, Opto Electronic Instrumentation, Remote Sensing, Satellite Communication System, System-on-chip, VLSI Design Technology, VHDL and Wireless Ad Hoc and Sensor Networks etc.

## COMPUTER SCIENCE AND ENGINEERING:

Advanced Semiconductor Process Technology, Artificial Intelligence, Architectural Support for Operating Systems, Binary Translation, Bioinformatics, Computer Vision, Data Mining, E-Commerce, E-governance, Design and Analysis of Algorithms, Embedded Processors, Expert Systems, Graphics, High Performance Microprocessors, Image Processing, Multimedia, Nano and Quantum Computing, Natural Language Processing, Neural Networks & Fuzzy Logic, performance evaluation, Pattern Recognition, Parallel and Distributed Processing, Robotics, Single Chip-Multiprocessors, Speech Processing, System Simulation & Modeling and Low-Power Chips etc.

## IMPORTANT DATES

15 August 2007	Submission of Full Paper
30 August 2007	Notification of Acceptance
10 September 2007	Submission of Camera- Ready Paper

## SUBMISSION INSTRUCTIONS

1. Prospective authors are encouraged to submit full papers written in English as per given submission instruction strictly, describing original, previously unpublished work, not currently under review by another conference or journal and submit well before the deadline.
2. One author can't submit more than one paper; registration for the conference is author wise & Number of authors per paper not to exceed two.
3. Your paper should be typed on one side only on A4 size white paper under 1 ½ spacing written in English in Times New Roman, 11 points with 2.5 cm margins on all four sides in MS Word. However, total number of pages should not exceed twenty (10) pages.
4. The paper should be in a well-structured, clear and concise presentation of the subject matter including an introduction, the main body and the conclusion with supporting tables and figures.
5. Heading and subheadings should be numbered as in any of the IEEE journals.
6. Figures and Tables should be neat and tidy, consecutively numbered, captioned and contained within the text at the appropriate places.
7. Centre at the top of the first page should be the complete title and the name(s), affiliations(s), mailing address (es), telephone no(s), and e-mail address(es) of the authors(s). Please indicate the intended presenter.
8. Reference suitably numbered should be provided at the end with their reference numbers given within square brackets at the appropriate places in the main text.
9. Your paper needs authorization from the Head of the Department of your study/ IEEE Branch Counsellor.
10. A bonafide certificate from your college/university.
11. Submit two copies of your full paper along with an abstract not exceeding 250 words.
12. In addition email a copy of the paper as an attachments (MS Word) to [nirbhay@ieee.org](mailto:nirbhay@ieee.org) before 15<sup>th</sup> August 2007.
13. Authors whose papers are selected are required to make a presentation at a special session of the symposia, subject to acceptance by the standard peer review process.
14. The papers will be included in the symposia proceedings.
15. A Self-Declaration: A declaration by the author/authors that "I/We declare that this paper has been completely compiled and prepared by me/us" and unpublished and not submitted in any other conference/symposia should must be enclosed.
16. Send your paper well before the schedule deadline.

## STUDENT PAPER SUBMISSION

Send Two hard copies of your full paper by Rgd. Post/Courier along with a DD/ local cheque of 100/- Rupees in favour of "IEEE Gujarat Section, payable at Ahmedabad", towards processing fee (non-refundable however will be adjusted in the Conference Fee).

To

**Nirbhay Chaubey**

Computer Science Department

Institute of Science And Tech. for Advanced Studies & Research

Nr. V. P. Science College, Vallabh Vidyanagar

Gujarat- 388120, India

E-mail: [nirbhay@ieee.org](mailto:nirbhay@ieee.org)

## CONFERENCE FEES FOR AISC-2007 & SPCTS-07

	AISC	SPCTS	AISC + SPCTS
IEEE-Members	600 Rs./-	400 Rs./-	900 Rs./-
Others		700 Rs./-	1500 Rs./-

Note:

1. Conference fee includes launch & refreshment for 2-3 days program, kits and conference material etc.
2. Registration Fee may be paid by DD/Local cheque in favour of IEEE Gujarat Section payable at Ahmedabad.

## ABOUT IEEE AND GUJARAT SECTION

The Institute of Electrical and Electronics Engineers popularly known as IEEE, founded in 1884 by Thomas Alva Edison, is the world's largest nonprofit, technical professional association with more than 386,000 individual members in 150 countries. The world is divided into 10 IEEE Regions. IEEE Gujarat Section is one of the 10 Sections in India coming under Asia-Pacific Region, the Region 10 of IEEE. It has three associated Society chapters, namely, the Computer Society Chapter, the Joint Chapter on Industry Applications, Industrial Electronics and Power Electronics Societies and System Man and Cybernetics Society. The Section has 8 Student Branches, namely Gujarat University Student Branch, Ahmedabad, M.S. University Student Branch, Baroda, Nirma Institute of Technology Student Branch, Ahmedabad, DA-IICT Student Branch, Gandhinagar, U.V.P.C.E. Student Branch, Mehsana, ADIT Student Branch, V.V.Nagar, GCET Student Branch, V.V.Nagar and ISTAR Student Branch V.V.Nagar. The section has been successfully organized many International and National Conferences including ICCN 1994, CNIW & CEEPCI 1998 and ADCOM-2004.

## ABOUT DA-IICT PROFILE

DA-IICT is an autonomous institute promoted by the Dhirubhai Ambani Foundation and the Reliance Group in pursuance of a Memorandum of Understanding entered into with the Government of Gujarat, to impart information and communication technology (ICT) education and research of international standards.

The intellectual ambience at DA-IICT is creating a technology knowledge hub and virtual feel that can trigger the scientific brain-power of the learners and researchers, and challenge them to shape up as knowledge workers and intellectual leaders. The green campus of DA-IICT is at Gandhinagar, the capital of the state of Gujarat. The city of Ahmedabad is about 25 km from the Institute, while the domestic and international airports are just about 15 km away.

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